

WHAT IS CLAIMED IS:

1. An interface circuit for USB data transfer, comprising:
a power source determination circuit for determining a type
5 of power source;

a power feeding switching circuit for setting an amount of
current to be supplied to a processing circuit conducting
processing for data transfer, based on a result of determination
made by the power source determination means; and

10 a clock switching circuit for setting operation clocks for
the processing circuit according to a result of determination made
by the power source determination means,

wherein

a data transfer rate is set according to the type of power
15 source.

2. The interface circuit according to claim 1, wherein

the power source determination circuit determines whether
an external power source is connected, and

20 when it is determined that an external power source is
connected, the power feeding switching circuit increases the
amount of current and the clock switching circuit sets a faster
clock, so that high speed data transfer is carried out, and

when it is determined that no external power source is
25 connected, the power feeding switching circuit causes to reduce
the amount of current and the clock switching circuit sets a slower
clock, so that slow speed data transfer is carried out.

3. The interface circuit according to claim 2, wherein the interface circuit operates as a USB ver. 2.0 interface circuit when an external power source is connected, and operates as a USB ver. 1.1 interface circuit when no external power source is connected.

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4. The interface circuit according to claim 3, which is registered as a USB ver. 2.0 interface circuit in a computer connected thereto when an external power source is connected, and as a ver. 1.1 interface circuit when no external power source is connected.

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5. A disk drive apparatus comprising the interface circuit according to claim 1, for writing data supplied from a computer onto a recording disk.

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6. The disk drive apparatus according to claim 5, wherein the disk drive apparatus sends the computer data concerning a data transfer rate at a time of resetting of the interface circuit.